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Attorney Doct. No.: 6211P001

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application for:

Johannes R. Gerardus De Vries

Serial No.: 08/966,954

Filing Date: November 10, 1997

For: DATA PROCESSING CIRCUIT,
MULTIPLIER UNIT WITH PIPELINE,
ALU AND SHIFT REGISTER UNIT
FOR USE IN A DATA PROCESSING
CIRCUIT

Examiner: Ngu, C.

Art Unit: 2787

State
2/15/05

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OFFICE OF PETITIONS

Box DAC

Assistant Commissioner for Patents
Washington, D.C. 20231

AMENDMENT AND RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action mailed on December 28, 1999, please enter the following amendment and consider the remarks below:

IN THE SPECIFICATION:

Please amend the specification as follows (referring to pages and lines of the Substitute Specification filed on Oct. 14, 1999):

Replace the paragraph beginning at page 2, line 26 with the following paragraph:

- a register unit provided with at least two registers for storing the integer data words of eight bits or multiples of a bits on which the operation and/or pipeline multiplication has to be performed; and

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